

In re the Application of

TI-12592A.23

Hashimoto, et al.

Div. of Serial No.: 10/452,744 Art Unit of prior case: 2188

Filed: March 31, 2004

Examiner of prior case: J. Lane

Title: Synchronous DRAM System with Control Data

Preliminary Amendment A Under 37 CFR 1.111

March 31, 2004

Asst. Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATE UNDER 37 C.F.R. §1.8(A) I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as #EV333321023US airbill to: Asst.

In the Specification:

Prior to examination, please insert the following sentence before line 1:

This application is a divisional of prior application 10/452,744, filed June 2, 2003, allowed February 24, 2004,

which was a divisional of prior application 09/745,892, filed December 21, 2000, now patent #6,418,078 issued July 9, 2002,

which was a divisional of prior application 08/488,231, filed June 7, 1995, now patent #6,188,635 issued February 13, 2001,

which was a divisional of prior application 08/362,289, filed December 22, 1994, now patent #5,636,176 issued June 3, 1997,

which was a divisional of prior application 08/175,478, filed December 29, 1993, now patent #5,400,288 issued March 21, 1995,